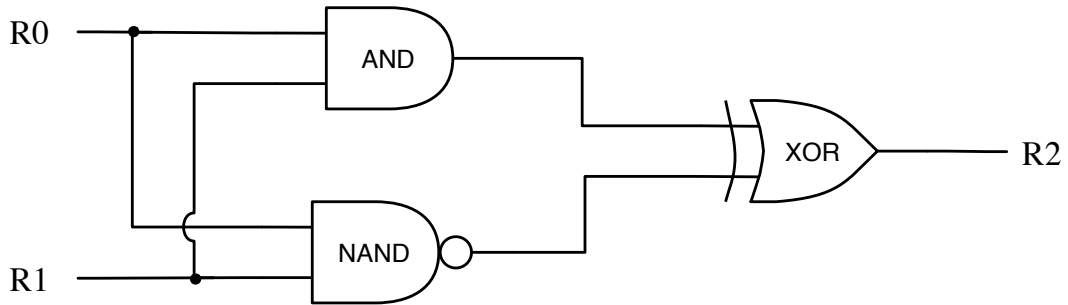


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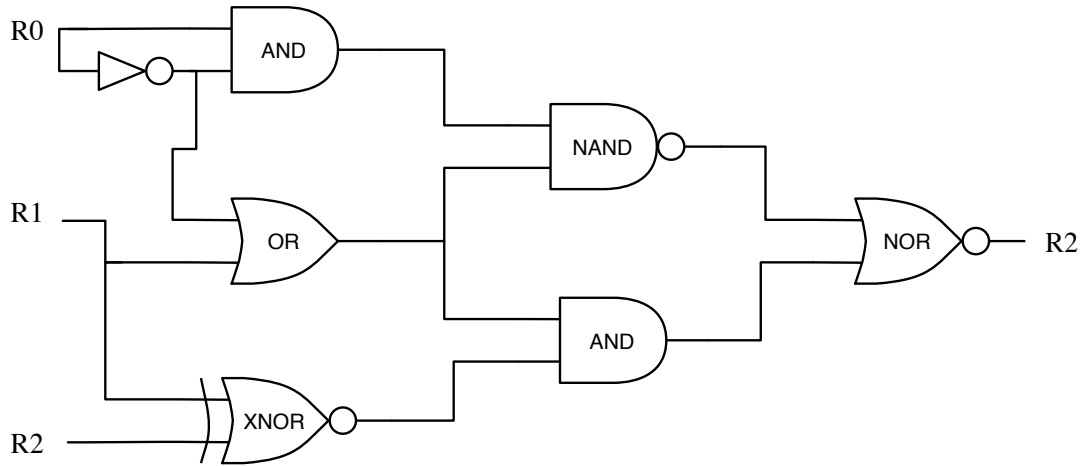
Lab 7

For the following problems it may be helpful to know that MVN Rx, Ry will copy into Rx the logical negation of Ry.

1. Write an ARM program that performs the following logical function:



2. Write an ARM program that performs the following logical function:



3. Write an ARM program that performs the following logical function: $R3 = \neg((R0 \wedge R1) \vee R2)$
4. Write an ARM program that performs the following logical function: $R2 = \neg(R0 \vee R1) \wedge (R2 \vee \neg R1) \vee (R1 \wedge R0)$
5. Write an ARM function that emulates a multiplexor. That is, R4 contains the value, R5 contains the selector value (which you can assume is between 0 and 15). Your function must write out the value in R4 to the register whose number is in R5. So, for instance, if R4 = 0xFEEDFACE and R5 = 12, your function should put the value 0xFEEDFACE into R12.