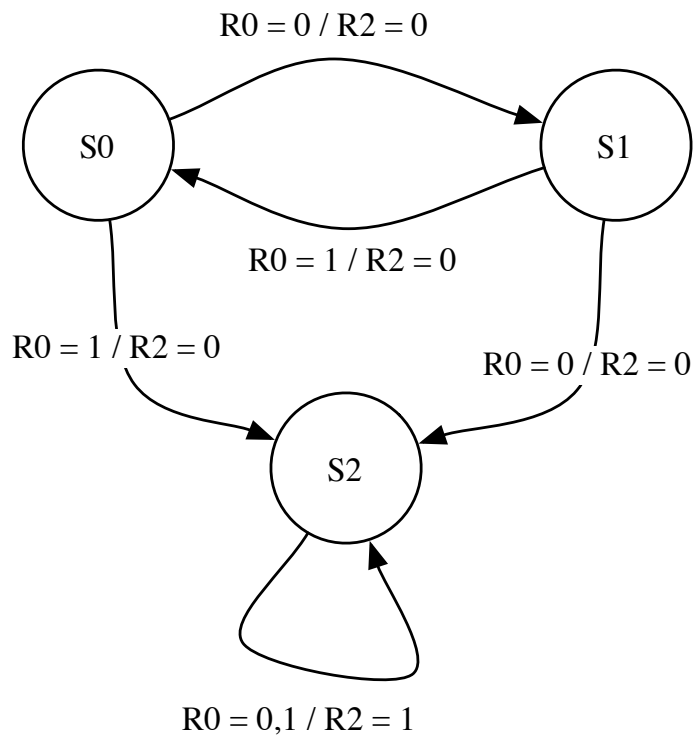


CmpSci 201

Lab 8

For the following state machines, R0 is the state machine input, R2 is the state machine output, and the actual state of the machine should be stored in R4. Implement the entire machine, do not simplify (even if possible).

1. Implement the following state machine in ARM assembly.



2. Implement the following state machine in ARM assembly.

